

Full Marks = 100

Problem 1: (Marks = 5x5 =25) Choose the correct option (only one) & provide analysis. No analysis will lead to 0 score. Only correct option and correct analysis will lead to full score. No step marks will be awarded.

Problem 1.1: A common source amplifier is shown in Fig. 1.1. The small signal parameters of M1 are g_{m1} , r_{o1} . The small signal voltage gain is

- a) $-g_{m1}R$
- b) $-g_{m1}r_{o1}$
- c) $-g_{m1}(r_{o1} || R)$
- d) $g_{m1}(r_{o1} || R)$

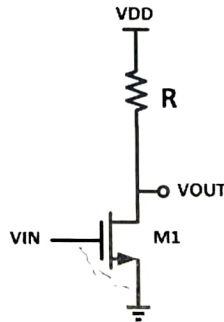


Fig. 1.1

Problem 1.2: The maximum and minimum swing of signal at the output of the amplifier in Fig. 1.1 is

- a) VDD and Overdrive voltage of M1
- b) VDD and V_{GS1}
- c) $VDD - I \cdot R$ and Overdrive voltage of M1
- d) $VDD - I \cdot R$ and V_{GS1}

Problem 1.3: In Fig. 1.3, an NMOS with aspect ratio of W/L has $V_{ov} = 0.2V$, $V_{th} = 0.4V$ and $I_d = 100\mu A$. Minimum V_D to keep M1 and M2 in saturation is

- a) 0.4V
- b) 0.6V
- c) 0.7V
- d) 1.4V

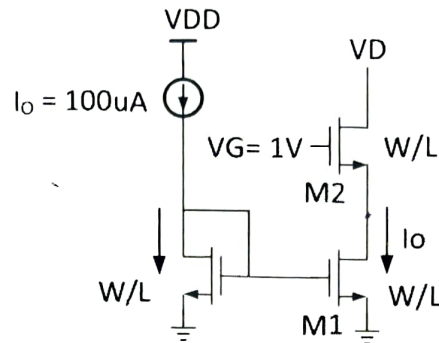


Fig. 1.3

Problem 1.4: A designer has tried all methods to optimize the noise performance of a common source amplifier with resistive load. To reduce the noise further he connects two of the stages in parallel node by node. Which of the following statement is true.

- The output noise rms voltage spectral density becomes half.
- The input referred noise rms voltage spectral density becomes half.
- Both (a) and (b) are correct
- The output noise current power spectral density becomes double.

Problem 1.5: A current mirror consists of two NMOS devices with $W = 4\mu\text{m}$ and $L = 1\mu\text{m}$. The A_{vt} of the technology is $5\text{mV}/\mu\text{m}$. The company manufactured 10^7 5G cellphone radio ICs which used this current mirror. Which of the following statement is true.

- In 68% of the ICs, the V_{th} mismatch will be within $\pm 2.5\text{mV}$.
- In 95% of the ICs, the V_{th} mismatch will be within $\pm 5\text{mV}$.
- In 99.7% of the ICs, the V_{th} mismatch will be within $\pm 7.5\text{mV}$.
- All of the above.

Problem 2: [Marks=7x5=35]

The circuit diagram of a common source amplifier is shown in Fig. 2. The small signal parameters of M1 are g_{m1} , r_{o1} and those of M2 are g_{m2} , r_{o2} . The threshold voltage of M1 and M2 are V_{th1} and $|V_{th2}|$ respectively. The thermal noise coefficient for both NMOS and PMOS is γ . Ignore flicker noise.

- Evaluate the small signal voltage gain of the circuit
- Evaluate the output impedance
- Evaluate the input impedance
- The maximum and minimum output voltage swing in terms of V_{GS1} , V_B , V_{th1} , $|V_{th2}|$.
- Evaluate the output noise voltage PSD. Ignore flicker noise.
- Evaluate the input referred noise voltage PSD.
- If PMOS M2 is replaced with an ideal current source I_d , plot small signal voltage gain vs I_d .

ELL735 (Analog Integrated Circuits) Minor Examination, 27th Sept 2022 (4:00-5:00 PM)
 Dept. of Electrical Engineering, IIT Delhi
 Note: Please assume missing parameters if any.

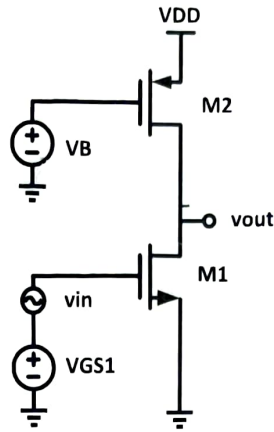


Fig. 2

Problem 3: [Marks=5+5+5+5=20]

The circuit diagram of a common source amplifier is shown in Fig. 3. The PMOS M2 is biased using a current mirror. The small signal parameters of M1 are g_{m1} , r_{o1} and those of M2/M3 are g_{m2} , r_{o2} . Thermal noise co-efficient is γ . Ignore flicker noise.

- Evaluate the expression for input referred noise voltage.
- For better SNR, how would you choose g_{m1} (small or large) and g_{m2} (small or large)?
- Given I_{bias} , how would you size M2/M3 for low noise [answer if small/large L, small/large W]? Explain why?
- Given I_{bias} , how would you size M2/M3 to minimize current mismatch due to both V_{th} and β mismatch [answer if small/large L, small/large W]? Explain why?

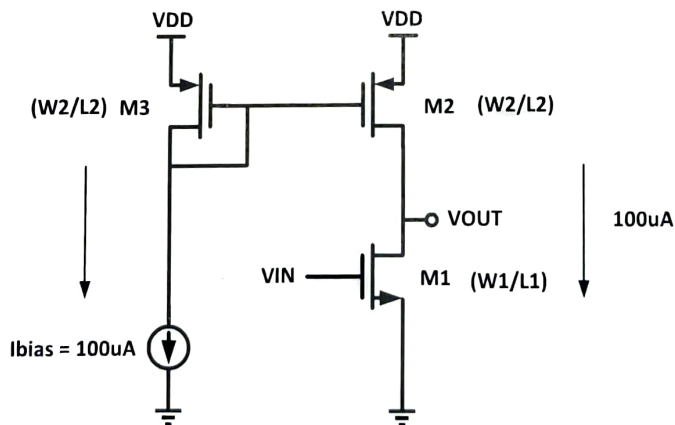


Fig. 3

Problem 4: [Marks= 5 + 5+ 10=20]

A biasing scheme of low voltage cascode current source is shown in Fig. 4. The small signal parameters of M1 are g_{m1} , r_{o1} ; those of M2 are g_{m2} , r_{o2} . Ignore body effect.

- Find the output impedance Z_{out} of the circuit in terms of small signal parameters of M1 and M2.
- Evaluate the expression for minimum voltage V_D at the output necessary to keep M1 and M2 transistors in saturation in terms of overdrives V_{ov1} and V_{ov2} .
- Evaluate the expression for minimum R value required to keep all transistors in saturation for this minimum voltage at the output. Express in terms of I_o , W_2/L_2 , C_{ox} and μ_n .

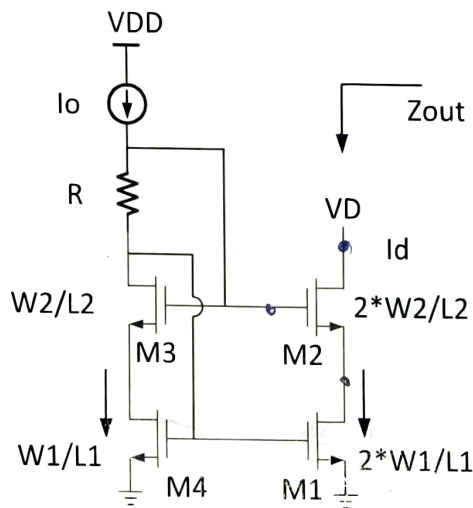


Fig. 4